# PATENT ABSTRACTS OF JAPAN

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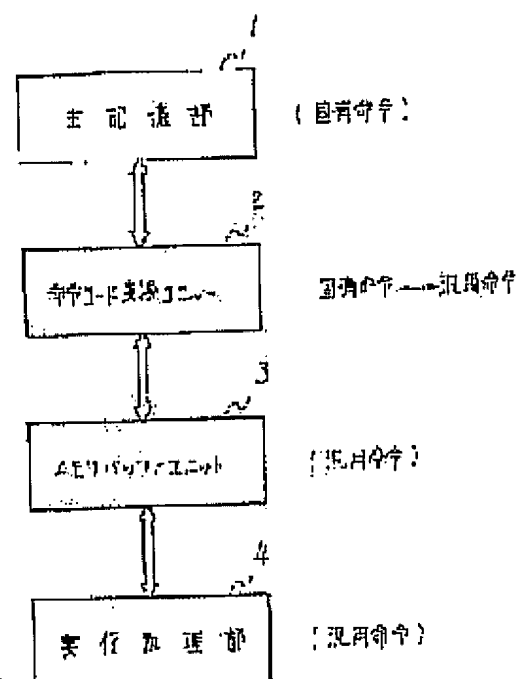| | |
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| (21) Application number : 02-054058 | (71) Applicant : NEC CORP |
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(54) CENTRAL PROCESSING UNIT

(57) Abstract:

PURPOSE: To appropriate other logical units (main storage part, memory buffer unit, and execution processing part) than an instruction code converting unit to a different kind of machine as they are by providing the instruction code converting unit in the central processor equipment.

CONSTITUTION: Between a main storage part 1 and a memory buffer unit 3, an instruction code converting unit 2 for converting an instruction code is provided. At the time of executing an instruction, an exclusive instruction code fetched from the main storage part 1 is converted to a general instruction code on the instruction code converting unit 2, and thereafter, sent to an execution processing part 4 through the memory buffer unit 3, and the execution processing part 4 executes its execution processing. In such a manner, other logical units than this instruction code converting unit 2 can be appropriated to the different kind of machine as they are, and the development manhour/ period of a new device can be shortened remarkably.

LEGAL STATUS

[Date of request for examination]